

<b>STUDY MODULE DESCRIPTION FORM</b>				
Name of the module/subject <b>Bases of electronics and the telecommunications</b>			Code <b>1010334511010337054</b>	
Field of study <b>Information Engineering</b>		Profile of study (general academic, practical) <b>general academic</b>	Year /Semester <b>1 / 1</b>	
Elective path/specialty -		Subject offered in: <b>Polish</b>	Course (compulsory, elective) <b>obligatory</b>	
Cycle of study: <b>First-cycle studies</b>		Form of study (full-time,part-time) <b>part-time</b>		
No. of hours Lecture: <b>8</b> Classes: - Laboratory: <b>16</b> Project/seminars: -			No. of credits <b>4</b>	
Status of the course in the study program (Basic, major, other) <b>other</b>			(university-wide, from another field) <b>university-wide</b>	
Education areas and fields of science and art			ECTS distribution (number and %)	
<b>Responsible for subject / lecturer:</b>  dr hab. inż. Tomasz Pajchrowski email: tomasz.pajchrowski@put.poznan.pl tel. 61 6652385 Wydział Elektryczny ul. Piotrowo 3A 60-965 Poznań			<b>Responsible for subject / lecturer:</b>  dr hab. inż. Tomasz Pajchrowski email: tomasz.pajchrowski@put.poznan.pl tel. 61 6652385 Wydział Elektryczny ul. Piotrowo 3A 60-965 Poznań	
<b>Prerequisites in terms of knowledge, skills and social competencies:</b>				
1	<b>Knowledge</b>	Basic knowledge of mathematics, physics and basics of electrical engineering [PRK 4]		
2	<b>Skills</b>	The ability to understand and interpret the knowledge transferred during classes. The ability to effectively self-educate in a field related to the chosen field of study. [PRK 4]		
3	<b>Social competencies</b>	Is aware of the need to broaden their competences, willingness to cooperate within the team [PRK 4]		
<b>Assumptions and objectives of the course:</b>  Getting acquainted with basic physical quantities and basic theory of electrical circuits. Getting acquainted with selected electronic and telecommunication systems used in Informatics.				
<b>Study outcomes and reference to the educational results for a field of study</b>				
<b>Knowledge:</b> 1. zna i rozumie podstawowe procesy zachodzące w cyku życia układów elektronicznych oraz programowalnych - [K1_W03 [P6S_WG] ]				
<b>Skills:</b> 1. Potrafi zbudować, uruchomić oraz przetestować proste układy elektroniczne - [K1_U08 [P6S_UW]] 2. Potrafi pozyskiwać informacje z literatury, baz danych i innych źródeł - [K1_U01 [P6S_UW]]				
<b>Social competencies:</b> 1. Ma świadomość ważności i rozumie pozatechniczne aspekty i skutki działalności inżyniera-informatyka i związaną z tym odpowiedzialność za podejmowane decyzje, jest gotów do dbałości o dorobek i tradycje zawodu - [K1_K02 [P6S_KR] ]				
<b>Assessment methods of study outcomes</b>				
<p>Lecture:  - evaluation of the knowledge and skills shown in a written colloquium on the theory of electrotechnics, electronics and telecommunications.</p> <p>Laboratory exercises:  - assessing the ability to prepare measurements of electronic and telecommunication circuits - checking the skills in each class and 1 colloquium during the semester.</p>				

<b>Course description</b>
Lecture with multimedia presentation (including: drawings, photos, animations, sound, films) supplemented by examples given on the board.
Theory presented in connection with the current knowledge of students.
Program content: History and basic concepts of electrical engineering. Electrical signals and their classification. Basic concepts of electric circuit with concentrated parameters. Basic elements and electronic systems. Mathematical models of electrical and electronic components. Basic knowledge of telecommunication systems and tracks. Transport media. Analysis of digital circuits in telecommunications.

**Basic bibliography:**

1. Bolkowski S. Teoria obwodów elektrycznych&#38;, WNT, Warszawa, 1998
2. Krakowski M. Elektrotechnika Teoretyczna. T.1, PWN, Warszawa, 1995
3. Doległa Marian, Podstawy elektrotechniki i elektroniki, WKŁ 2016
4. Wesołowski K. Podstawy cyfrowych systemów telekomunikacyjnych, WKŁ, 2006

**Additional bibliography:**

1. Mikołajuk K., Trzaska Z. ,Zbiór zadań z elektrotechniki teoretycznej, WNT, W-a, 1978
2. Chua L.O., Desoer C.A., Kuh E.S. Linear and Nonlinear Circuits, McGraw-Hill Inc., 1987

**Result of average student's workload**

<b>Activity</b>	<b>Time (working hours)</b>
1. Participation in lectures	8
2. Participation in laboratory	16
3. Participation in consultations on the lecture	2
4. Participation in consultations concerning the laboratory	4
5. Preparation for the exam	34
6. Exam	2
7. Preparation for laboratory classes, preparation of reports	36

**Student's workload**

<b>Source of workload</b>	<b>hours</b>	<b>ECTS</b>
Total workload	102	4
Contact hours	32	1
Practical activities	52	2